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MAILING DATE: 8/20/2003 ATTORNEY DOCKET NO.: 200209781-1

SERIAL NO.: TITLE:

Data Compression in Multiprocessor Computers

INVENTOR(S):

Judson E. Veazey

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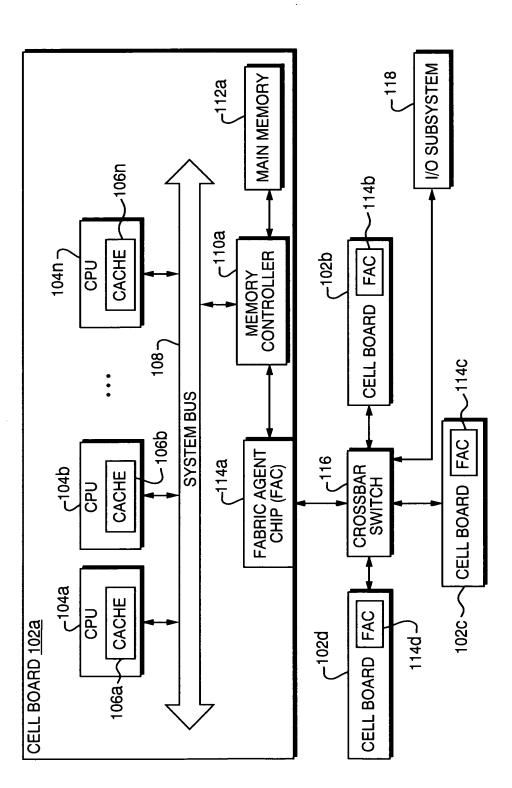
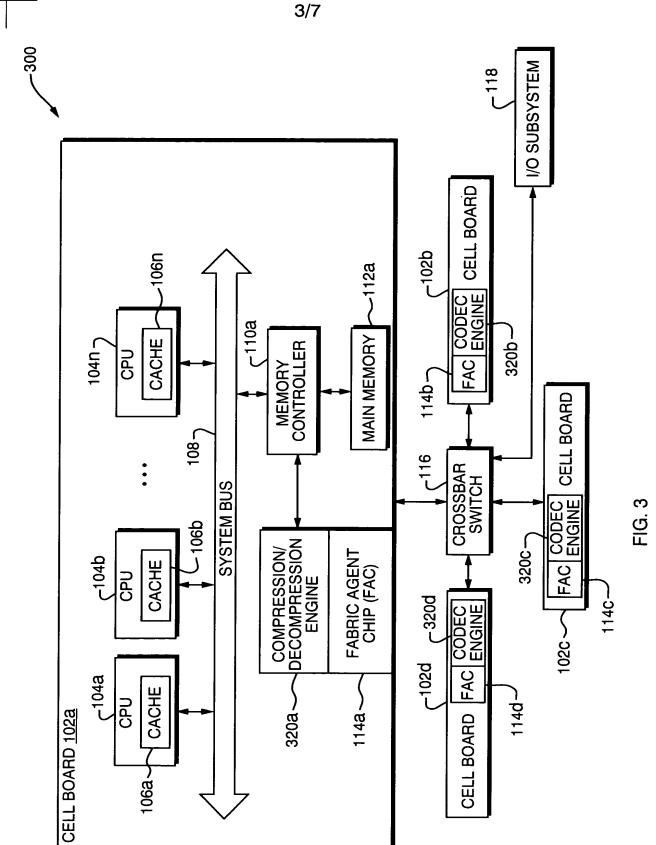


FIG. 1 (PRIOR ART

EXPRESS MAIL NO.: EV148864618US MAILING DATE: 8/20/2003 SERIAL NO.: NOT YET ASSIGNED ATTORNEY DOCKET NO.: 200209781-1 TITLE: **Data Compression in Multiprocessor Computers** INVENTOR(S): Judson E. Veazey 2/7 208 CROSSBAR SWITCH READ DATA -206 206 FABRIC AGENT CHIP / READ REQUEST -114a / READ REQUEST 208 -114d READ DATA 208 FABRIC AGENT CHIP -204 / READ REQUEST FIG. 2 (PRIOR ART) READ DATA MEMORY CONTROLLER ~110a / READ REQUEST 208 208 -110d 208 CONTROLLER MEMORY MAIN MEMORY / READ REQUEST READ DATA READ DATA READ DATA CPU

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Judson E. Veazey INVENTOR(S):

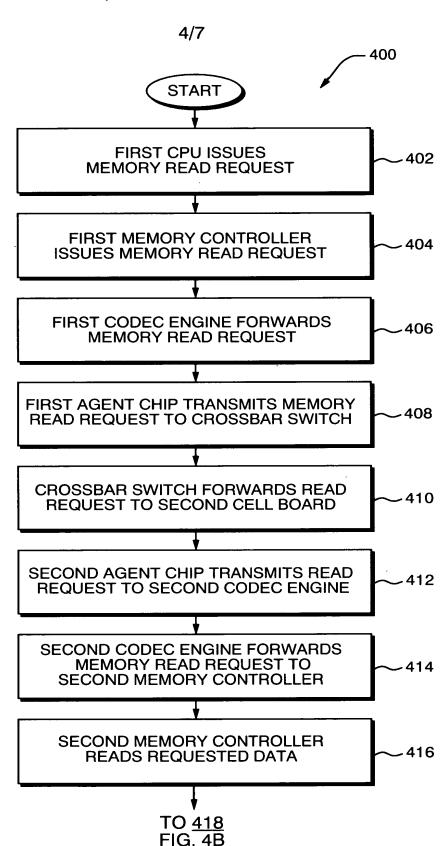


FIG. 4A

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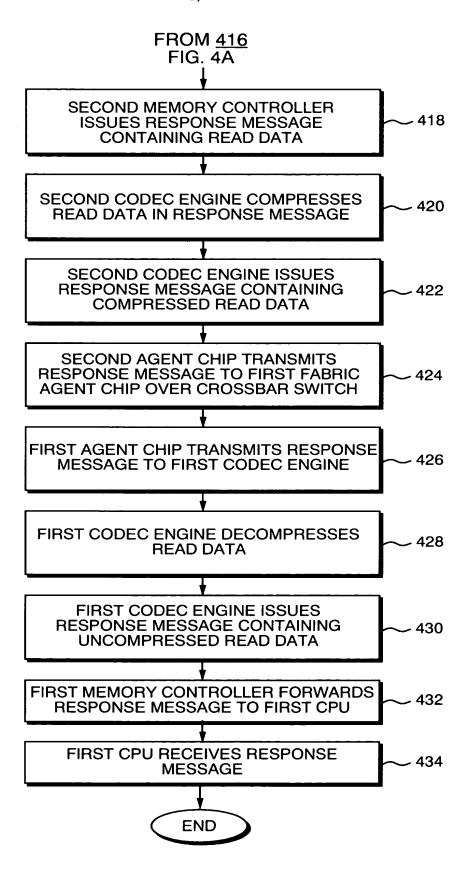
TITLE:

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SERIAL NO.: INVENTOR(S):

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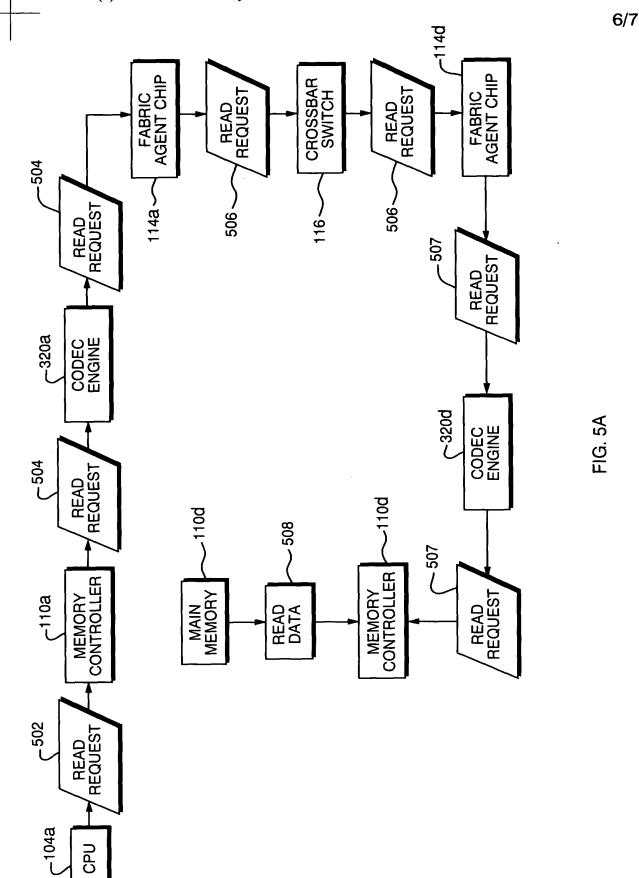
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Judson E. Veazey

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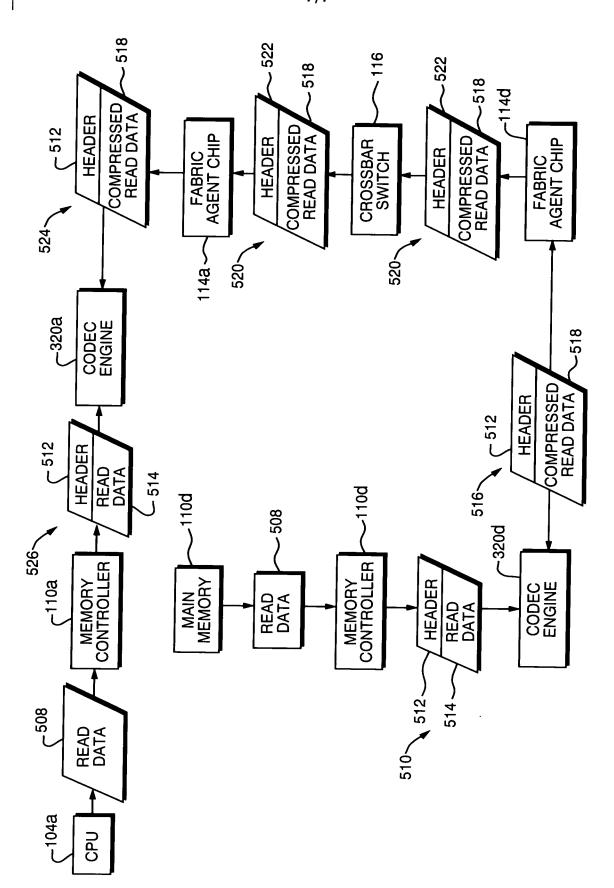


FIG. 5B